Datasheet Series PLI



Model PLI4		812	H&H
Order no.	17-022	-000-02	
Basic operating modes		CC, CV, CR, CP	
Standard interfaces		F	RS-232, USB, LAN, CAN
Max. input voltage Vmax			120 V
Min. input voltage Vmin 1)		1.2 V	
Max. load current Imax		225 A	
Continuous power		4800 W	
Short-time power ²⁾ 9600 W		9600 W	
Voltage setting		0 120 V	
Current ranges			0 225 A
Resistance ranges		0.00	0889 Ohm 5.73515 Ohm
Power ranges continuous/short-time 3)		0 9600 W	
Rise and fall time fast / medium / slow 4)			20 / 150 / 2000 μs
Load terminals (front) 5)			-
Load terminals (rear) ⁶⁾			FKS25/10-SM10
Mains voltage 7)		1/N	N/PE AC 230 V 50 60 Hz
Mains voltage toggleable ⁸⁾		1/N/PE AC 115 V 50 60 Hz	
Power consumption		190 VA	
Noise max. ca. 9)			71 dB(A)
Weight ca.		39 kg	
Housing / 3D model ¹⁰⁾			19" - 5 U / PLI_M19
Width x Height x Depth 483 x 222 x 635 mm		483 x 222 x 635 mm	

- 1. Minimum input voltage for maximum static load current.
- 2. Level and duration of the peak power depend on the previous power.
- 3. The setting range extends max. to the possible peak power.
- 4. Rise and fall times are defined of 10 % ... 90 % and 90 % ... 10 % of the maximum current (CC mode, fast regulation speed, tolerance ±20 %). Rise and fall time at setting "medium": ca. 150 μ s, "slow": ca. 2 ms.
- $5. \quad \mathsf{BPK4-30L:} \ \mathsf{Touch-protected} \ \mathsf{binding} \ \mathsf{posts} \ \mathsf{for} \ \mathsf{4} \ \mathsf{mm} \ \mathsf{laboratory} \ \mathsf{jacks} \ \mathsf{and} \ \mathsf{stripped} \ \mathsf{wires} \ \mathsf{with} \ \mathsf{diameter} \ \mathsf{up} \ \mathsf{to} \ \mathsf{4} \ \mathsf{mm}, \ \mathsf{max.} \ \mathsf{30} \ \mathsf{A} \\ \mathsf{max} \ \mathsf{and} \ \mathsf{stripped} \ \mathsf{wires} \ \mathsf{with} \ \mathsf{diameter} \ \mathsf{up} \ \mathsf{to} \ \mathsf{4} \ \mathsf{mm}, \ \mathsf{max.} \ \mathsf{30} \ \mathsf{A} \\ \mathsf{max} \ \mathsf{and} \ \mathsf{and}$ BPK4-60L: Touch-protected binding posts for 4 mm laboratory jacks and stripped wires with diameter up to 6 mm, max. 60 A FKS20/5-SM8: Flat copper bars 20 x 5 mm vertical with hole for screw M8
 - FKS25/8-SM10: Flat copper bars 25 x 8 mm vertical with hole for screw M10
 - FKS25/10-SM10: Flat copper bars 25 x 10 mm vertical with hole for screw M10
 - FKS40/12-SM12: Flat copper bars 40 x 12 mm vertical with hole for screw M12

Datasheet Series PLI



Models with copper bars (FKS) are delivered with safety covers.

- 6. BPK4-30L: Touch-protected binding posts for 4 mm laboratory jacks and stripped wires with diameter up to 4 mm, max. 30 A BPK4-60L: Touch-protected binding posts for 4 mm laboratory jacks and stripped wires with diameter up to 6 mm, max. 60 A FKS20/5-SM8: Flat copper bars 20 x 5 mm vertical with hole for screw M8 FKS25/8-SM10: Flat copper bars 25 x 8 mm vertical with hole for screw M10 FKS25/10-SM10: Flat copper bars 25 x 10 mm vertical with hole for screw M10 FKS40/12-SM12: Flat copper bars 40 x 12 mm vertical with hole for screw M12 Models with copper bars (FKS) are delivered with safety covers.
- 7. Mains voltage tolerance: $\pm 10~\%$
- 8. Mains voltage tolerance: ±10 %
- 9. Measured on the front from distance of 1 m.
- 10. Largest width and depth without wiring. 1 U = 44.45 mm.

PLI Series Technical Data

Operating modes					
Basic operating	CC, CV, CR, CP				
modes					
Combined opera- ting modes	CC+CV, CR+CC+CV, CP+CC+CV, CV+CC				
Accuracy of setting					
	of setting		of corresponding range		
Voltage	±0.2 %		±0.05 %		
Current	±0.2 %		PLI MR in R1 ±0 others ±0.05 %	1.1 %,	
Resistance (at 5 % to 100 % of voltage range)	±1.4 %		±0.3 % of curre	nt range	
Power	PLI EC	others	PLI EC	others	
(at V and I > 30 % of range)	±1 %	±0.35 %	±0.3 %	±0.1 %	
(at V and I > 5 % and < 30 % of range)	±2 %	±0.7 %	±0.75 %	±0.25 %	
Resolution	14 bits				
Accuracy of adjustable					
, ,	of setting		of corresponding r	ange	
Overcurrent pro-					
tection	±1.4 %		±0.3 %	±0.3 %	
Undervoltage protection	±1.4 %		±0.3 %		
Resolution	12 bits				
Accuracy of measurement slow					
	of measured value (real value)		of corresponding range		
Voltage	±0.01 %		±0.005 %		
Current	±0.2 %		PLI MR in R1 ±0 others ±0.05 %	1.1 %,	
Resistance	is calculated from current and voltage				
Power	is calculated from current and voltage				
Resolution	23 bits				
Sampling time	250 ms, not t	riggerable			
Accuracy of display					
Number of decimal places	5				
Accuracy	Accuracy of measurement slow ±1 digit of the display value				
Accuracy of measurement fast					
	of measured value (real value) of corresponding range		ange		
Voltage	±0.1 %		±0.05 %		
Current	±0.2 %		PLI MR in R1 ±0 others ±0.1 %	1.2 %,	
External control voltage	±0.2 %		±0.1 %		
Resistance	calculated from voltage and current values				
Power	calculated from voltage and current values				
Resolution	16 Bit				
Sampling time	200 μs 1000 s				
Accuracy of trigger vo	ltage and current				
Voltage	±1 % of range				
Current	±1 % of range				
Dynamic function (LIS	T)				
No. of load levels	max. 300, with ramp and dwell time setting				
	min.		max.		
Dwell time	200 μs		1000 s		
Ramp time	0 s		1000 s		
Resolution Accuracy of the	200 μs				
setting times Delay at triggered	±0.02 %				
start	max. 300 μs				

0.5 to 30 s, resolution 0.1 stimestamp, voltage, currentlimited by USB memory calcsv 200 µs 1000 s, resolution dynamic function timestamp, voltage, currently	nt
timestamp, voltage, currer limited by USB memory ca .csv 200 µs 1000 s, resolutio dynamic function	nt apacity
limited by USB memory ca .csv 200 µs 1000 s, resolutio dynamic function	apacity
.csv 200 µs 1000 s, resolutio dynamic function	
200 μs 1000 s, resolutio dynamic function	n 200 μs, synchronized with
dynamic function	n 200 μs, synchronized with
dynamic function	n 200 μs, synchronized with
timestamp, voltage, curre	
1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1	nt
max. 40,000	
9, selectable (incl. program 1 for last device settings a	
alog control 0 10 V	
of setting	of corresponding range
±0.2 %	±0.1 %
±0.2 %	PLI MR in R1 ± 0.2 %, others ± 0.1 %
±1.6 %	±0.4 % of current range
±0.55 %	±0.2 %
±0.9 %	±0.35 %
±1 %	±0.4 %
±1 %	±0.4 %
Input resistance of analog	inputs >10 kΩ
alog monitor outputs 0 10) V
of analog signal of real value	offset voltage
±0.2 %	±15 mV
±0.2 %	±15 mV
load capacity minimal 2 $k\Omega$	
oltages	
standard I/O port	isolated I/O port (option PLIO6)
PLIxxxxZV: must be galvanically isolated	PLIxxxxZV: max. 2 V ¹⁾ all others: max. 800 V ¹⁾
	max. 125 V ¹⁾
	1 for last device settings a alog control 0 10 V of setting ±0.2 % ±0.2 % ±1.6 % ±0.55 % ±0.9 % ±1 % Input resistance of analog alog monitor outputs 0 10 of analog signal of real value ±0.2 % ±0.2 % load capacity minimal 2 kiltages standard I/0 port PLIxxxxZV: must be

The specified accuracies refer to an ambient temperature of 23 ± 5 °C. The specified accuracies are valid when the sense lines are connected and when the unit is connected to undisturbed voltages (ripple and noise < 0.1 %). At voltages with higher disturbance values the accuracy can change for the worse.

Technical Data

I/O port: control outpu	ts and inputs
Outputs	activation state load input (low active) status overload (OV, OCP, OPP, OTP, low active) trigger output (low active) programmable logic output (by SCPI command)
Output level	selectable, 3.3 V, 5 V, 12 V or externally programmable up to 30 V $$
Control inputs	activation state load input (low active) operating mode selection trigger input (high active) readable logic input (by SCPI command) control input (activates the analog signals, low active) remote shut-down (low active)
input level	3 30 V

$ \begin{array}{c} \text{Input resistance} & > 50 k\Omega \text{when load input is off} \\ \text{diode function at reverse polarity up to nominal current,} \\ \text{except ZV models} \\ \\ \text{Input capacity} & \text{see model overview} \\ \end{array} $	Input
Input capacity see model overview	Input resistance
	Input capacity
Parallel operation up to 5 devices in Master-Slave operation	Parallel operation
Max. input voltage see model overview	Max. input voltage
Min. input voltage see model overview	Min. input voltage

Input: permissible voltages

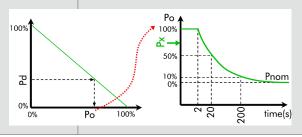
	standard I/O port	isolated I/O port (option PLIO6)
Vin-PE (neg. load	max. 125 V ¹⁾	PLIxxxxZV: max. 125 V ¹⁾
input - PE)		all others: max. 800 V ¹⁾
Vin+PE (pos. load input - PE)	Vmax + max. 125 V ¹⁾	PLIxxxxZV: Vmax + max. 125 V ¹⁾ all others: Vmax + max. 800 V ¹⁾

Power

Continuous power

Derating	-1.2 %/°C for Ta > 21 °C
Overload capability (short-time power)	see model overview The max. possible overload Po depends on the temperature of the device and therefore on the previously consumed continuous power Pd. The possible overload duration depends on the value of the overload Px.

see model overview (at Ta = 21 °C)



Protection and monitoring

Sense

Protec	tive devices	overcurrent overpower overtemperature
Monito	oring	overvoltage indication reverse polarity indication undervoltage indication (if the input voltage is too low for the set current)
Terminals		
Load in	nput	see model overview

Operating conditions	
Operating temperature	5 40 °C
Stock temperature	-25 65 °C
Max. operating height	2,000 m above sea level
Pollution degree	2
Overvoltage category of mains	П
Max. humidity	80 % at 31 °C, linear decreasing to 50 % at 40 °C
Min. distance rear panel to wall or other objects	70 cm
Cooling	3-stage air cooling, up from 3200 W variably controlled
Noise. weight	see model overview
Mains voltage with option PLI18	see model overview 11 15 V DC
Mains cable	length max. 3 m cross-section of mains leads min. 1 mm²
Power consumption	see model overview

Housing	
Color Front Rear Top, side panels	RAL7035 (light grey) stainless steel RAL7037 (dusty grey)
Safety and EMC	
Protection class	1
Measuring category	O (CAT I according to EN61010:2004)
Electrical safety	DIN EN 61010-1 DIN EN 61010-2-030
EMC	DIN EN 61326-1 DIN EN 55011 DIN EN 61000-3-2 DIN EN 61000-3-3
Standard interfaces	
Nata interfaces	RS-232 LISB LAN CAN

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Data interfaces	RS-232, USB, LAN, CAN
I/O port	standard I/O port (not isolated)
Available options	
Data interfaces	GPIR

Mechanical options	
PLI10	19" installation kit for 1 device with ½ 19", 2 U
PLI11	19" installation kit for 2 devices with ½ 19", 2 U
PLI12	19" installation kit for 1 device with 19", 2 U
PLI13	19" installation kit for 1 device with 19", 3 U
PLI14	heavy-load castors (5 U and upwards)
Function enhance- ment PLI21 Accuracy	MPPT function with activation code see accuracy of measurement fast
Hardware extensions	
PLI06	galvanically isolated I/O port
PLI16-06 PLI16-12 Accuracy Load current Activation Activation time	Charger Starter Interface (CST) for 60 V models (660 V) Charger Starter Interface (CST) for 120V models (6120V) ±1 % ±200 mV max. 0.1 A can be coupled with activation state of load input 0.1 100 s ±0.3 s
PLI17	switch box for external load activation via I/O port
DC mains supply PLI18	12 V DC mains supply (only for PLI14xx)

Calibration, warranty	
FCC-PLIxx	Factory Calibration Certificate, twice for free
Warranty	2 years

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